

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of: KOERTZEN et al.)	Examiner: Not yet assigned
)	
Serial No.: Not yet assigned)	Group Art Unit: Not yet assigned
)	
Filing Date: Herewith)	INTEL Docket No.: P17683
)	
For: ADJUSTABLE ACTIVE VOLTAGE)	
POSITIONING SYSTEM)	
)	
)	
)	
)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants submit herewith patents, publications or other information of which they are aware that they believe may be material to the examination of this application, and in respect of which, there may be a duty to disclose.

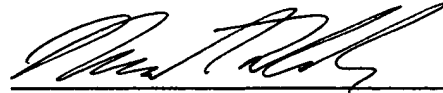
The filing of this information disclosure statement shall not be construed as a representation that a thorough search has been made, an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists. Nor shall the filing of this information disclosure statement be construed as an admission against interest in any manner.

This Information Disclosure Statement is filed in accordance with 37 CFR §§1.56, 1.97 and 1.98. The items listed on the accompanying Form PTO-1449 may be deemed to be pertinent to the above-identified application and are made of record to assist the Patent and Trademark Office in its examination of this application. The Examiner is respectfully requested to fully consider the items listed on the enclosed copy of Form PTO-1449 and to independently ascertain their teaching.

No fee is believed to be due under 37 CFR §1.17(p) for this Information Disclosure Statement since it is being filed concurrently with the above-identified application.

Respectfully submitted,

January 5, 2004
Date



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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Applicati n Numb r	Not yet assigned
				Filing Dat	Herewith
				First Named Inventor	KOERTZEN, Henry W.
				Group Art Unit	Not yet assigned
				Examiner Name	Not yet assigned
				INTEL Docket Number	P17683
				Sheet	1

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Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Applicant Number	Not yet assigned
				Filing Date	Herewith
				First Named Inventor	KOERTZEN, Henry W.
				Group Art Unit	Not yet assigned
Examiner Name	Not yet assigned				
Sheet	2	of	2	INTEL Docket Number	P17683

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	Huang, Wenkang et al.: "A Scalable Multiphase Buck Converter with Average Current Share Bus", International Rectifier, APEC 2003, 7pp.	
	2	Huang, Wenkang et al.: "System Accuracy Analysis of the Multiphase Voltage Regulator Module", International Rectifier, APEC 2003, 7pp.	
	3	"Intel Mobile Voltage Positioning Technology Overview", August 31, 1999, 5pp.	
	4	Dettwiler, Steven, "Keeping Transients and Static in Check", Intel® DeveloperUPDATE Magazine, April 2001, 5pp.	
	5	Linfinity Application Note AN-10, "Design Procedure for Microprocessor Buck Regulators", Copyright © 1998, Rev. 0.2.1. 07/98, 13pp.	
	6	"Intel® Pentium® 4 Processor VR-Down Design Guidelines", November 2002, Order Number 249891-004, 16pp.	
	7	Intel® APP-933, Application Note, "Flexible Motherboard Power Distribution and Control for the Pentium® III Xeon™ Processor", January 2000, Order Number 245245-002, 25pp.	
	8	Freeman, Dave et al. "Mobile Devices: RF demands shrink power window", May 16, 2001, CommsDesign, download from http://www.commsdesign.com/printableArticle?doc_id=OEG20010516S0008 on 11/20/2003, Copyright © 2003, CMP Media, LLC.	

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